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TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
FIS9-2000-0372-US1

In Re Application Of: S. Sundar Kumar Iyer et al.

#11/2
1/12/04

Serial No.	Filing Date	Batch No.	Examiner	Art Unit
09/878,556	June 11, 2001	Unassigned	Unassigned	Unassigned

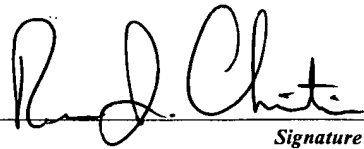
Invention: METHOD OF FORMING VARIABLE OXIDE THICKNESSES ACROSS SEMICONDUCTOR CHIPS

Address to:
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Washington, D.C. 20231

Transmitted herewith are:

2 sheets of formal drawing(s) for this application.

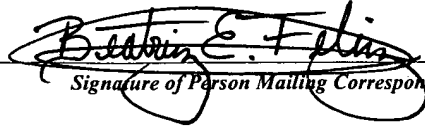
Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c) on the reverse side of the drawing.


Signature

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Dated: June 28, 2001

I certify that this document and attached formal drawings are being deposited on 6/28/01 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.


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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)

Applicant(s): S. Sundar Kumar Iyer et al.

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FIS9-2000-0372-US1

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Examiner

Unassigned

Group Art Unit

Unassigned

Invention: Method of Forming Variable Oxide Thicknesses Across Semiconductor Chips

I hereby certify that this Transmittal of Formal Drawings

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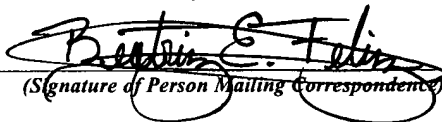
Commissioner of Patents and Trademarks, Washington, D.C. 20231-0001 on

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